

Copy showing Schedule

Odd Semester Examination 2018-19

M.Tech. I Yr./I Sem						
Sr.No	Subject Code	Subject Name	Teacher's Name	Date	Time	Venue
1	EC-501	Semiconductor Device Modeling & Circuits Simulation	Dr. Shailendra Kumar	21/12/18	10 :00-11:00AM	CAD Lab
2	EC-502	VLSI Devices & Circuits	Dr. Savita Maurya	22/12/18	11:30 - 12:30 PM	CAD Lab
3	EC-503	Microelectronics & Technology	Dr. Imran U Khan	21/12/18	9 :00-10:00AM	CAD Lab
4	EC-504	Digital System Design using Verilog HDL	Mohammad Javed Khan	20/12/18	10 :00-11:00AM	Room No. 304